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By Donald Givone

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The emphasis in the book is on the thorough presentation of basic principles of logic design and the illustration of these principles. While many introductory texts only provide the mechanics of classical logic design, Givone provides justifications behind these procedures to give students the understanding they need for the advanced topics they will learn about in subsequent courses. Some of the topics that the book thoroughly presents include: the simplification of Boolean expressions with Karnaugh maps, variable-entered Karnaugh maps, and the analysis and design of both clocked synchronous sequential networks and asynchronous sequential networks.

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Digital Principles and Design with CD-ROM By Donald Givone Bibliography

- Sales Rank: #326840 in Books
- Published on: 2002-07-10
- Original language: English
- Number of items: 1
- Dimensions: 9.00" h x 1.20" w x 7.00" l, 2.75 pounds
- Binding: Hardcover
- 751 pages

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Editorial Review

About the Author

Givone received his B.S.E.E. degree from Rensselaer Polytechnic Institute and the M.S. and Ph.D. degrees in Electrical Engineering from Cornell University. In 1963, he joined the faculty at the University of Buffalo, where he is currently a Professor in the Department of Electrical Engineering.

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