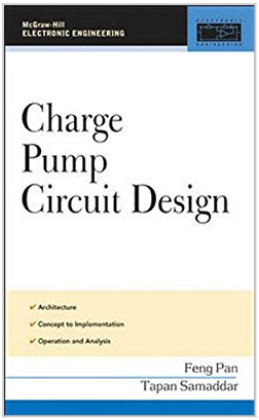

Charge Pump Circuit Design (McGraw-Hill Elctronic Engineering)

By Feng Pan, Tapan Samaddar

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Charge pumps are finding increased attention and diversified usage in the new era of nanometer-generation chips used in different systems. This book explains the different architectures and requirements for an efficient charge pump design and explains each step in detail. It's filled with extra hands-on design information, potential pitfalls to avoid, and practical ideas harnessed from the authors' extensive experience designing charge pumps.

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Charge Pump Circuit Design (McGraw-Hill Elctronic Engineering) By Feng Pan, Tapan Samaddar Bibliography

- Sales Rank: #2866839 in eBooks
- Published on: 2010-08-10
- Released on: 2010-08-10
- Format: Kindle eBook

Editorial Review

From the Back Cover
The First-Ever Guide to Designing and Implementing Charge Pumps for Today's Low-Cost, High-Performance Mobile Devices!

A groundbreaking tool for circuit design engineers, *Charge Pump Circuit Design* is the first book to focus solely on the design and implementation of charge pumps used in EEPROMs, Flash memory, White LED drivers, and a myriad of other circuits finding mass applications in PDAs, digital cameras, MP3 players, video recorders, cell phones, USB drives, and more.

Written by two of today's leading circuit designers, *Charge Pump Circuit Design* explores the basic operations, design criteria, and newest approaches for designing state-of-the-art charge pumps. The authors explain the different architectures and requirements, providing comprehensive information for each stage in the design process. Filled with 100 detailed illustrations, this time-saving reference also presents a wealth of practical design tips and potential pitfalls to avoid.

***Charge Pump Circuit Design* features:**

- The latest design techniques for creating highly efficient charge pumps for any type of application requirement
- Step-by-step guidelines for completing a charge pump design -- from initial concept to implementation of actual layout
- Thorough mathematical derivations and analyses of operations that are applicable to all charge pump requirements, regardless of the system being designed

Inside This Landmark Design Reference:
* History of High-Voltage Charge Pumps * Basic Operations of Charge Pumps * Criteria of a Generic Charge Pump * How to Design a Basic Charge Pump * How to Design a Better Charge Pump * Charge Pump Architectures * Future Design Reference

About the Author
Tapan Samaddar is a design engineer at SanDisk Corporation, where he leads the company’s high-voltage circuit designs. Previously, he was employed by T-RAM, Inc., where he was involved in the design of high-speed SRAM-compatible memory chips. He is a holder of several U.S. design patents. Mr. Samaddar has also worked on high-density NOR Flash memory at Atmel Corporation, and on high-speed cache memory designs at ST Microelectronics.

Feng Pan is doing circuit design at SanDisk Corporation, where he is involved in defining the architecture and implementing charge pumps and related high-voltage circuits over several generations of NAND Flash memory products. He is aholder of 17 U.S. design patents. Mr. Pan previously worked at AMD, where he contributed to the company's NOR Flash memory chip designs. He holds an MS degree from Stanford University and a BS degree from U.C. Berkeley.

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