Weaving High Performance Multiprocessor Fabric: Architectural Insights to the Intel QuickPath Interconnect

By R. Maddox, G. Singh, R. Safranek, R. Colwell

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Welcome to the era of the Intel® QuickPath Interconnect!

Weaving High Performance Multiprocessor Fabric is written for hardware design, validation and BIOS engineers to introduce the compelling mix of performance and features in the Intel® QuickPath Interconnect.

From the foreword, Robert P. Colwell opines: "Even for inveterate geeks like me, most technical books are dry as dust and work much better than insomnia pills. They should come with warning stickers: Do not operate heavy machinery for a week after reading this book. Not this book though: Weaving High Performance Multiprocessor Fabric is engaging, educational, well-organized and directly useful. It doesn't get any better than that." It explains the Intel® QuickPath Interconnect, which provides the foundation for future generations of Intel® microprocessor systems with a high-speed, packetized, point-to-point system interconnect that uses multiple narrow high-speed links to stitch together processors and IO hubs into a fabric of a distributed shared memory-style platform architecture.

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