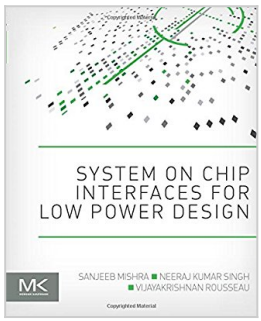


# System on Chip Interfaces for Low Power Design

*By Sanjeeb Mishra, Neeraj Kumar Singh, Vijayakrishnan Rousseau*



**System on Chip Interfaces for Low Power Design** By Sanjeeb Mishra, Neeraj Kumar Singh, Vijayakrishnan Rousseau

System on Chip Interfaces for Low Power Design provides a top-down understanding of interfaces available to SoC developers, not only the underlying protocols and architecture of each, but also how they interact and the tradeoffs involved. The book offers a common context to help understand the variety of available interfaces and make sense of technology from different vendors aligned with multiple standards. With particular emphasis on power as a factor, the authors explain how each interface performs in various usage scenarios and discuss their advantages and disadvantages. Readers learn to make educated decisions on what interfaces to use when designing systems and gain insight for innovating new/custom interfaces for a subsystem and their potential impact.

- Provides a top-down guide to SoC interfaces for memory, multimedia, sensors, display, and communication
- Explores the underlying protocols and architecture of each interface with multiple examples
- Guides through competing standards and explains how different interfaces might interact or interfere with each other
- Explains challenges in system design, validation, debugging and their impact on development

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**System on Chip Interfaces for Low Power Design By Sanjeeb Mishra, Neeraj Kumar Singh, Vijayakrishnan Rousseau Bibliography**

- Rank: #3000744 in Books
- Published on: 2015-12-22
- Released on: 2015-12-08
- Original language: English
- Number of items: 1
- Dimensions: 9.25" h x .92" w x 7.50" l, 1.86 pounds
- Binding: Paperback
- 406 pages

## Editorial Review

From the Back Cover
*System on Chip Interfaces for Low Power Design* provides a top-down understanding of interfaces available to SoC developers, not only the underlying protocols and architecture of each, but also how they interact and the tradeoffs involved. The book offers a common context to help understand the variety of available interfaces and make sense of technology from different vendors aligned with multiple standards. With particular emphasis on power as a factor, the authors explain how each interface performs in various usage scenarios and discuss their advantages and disadvantages. Readers learn to make educated decisions on what interfaces to use when designing systems, and gain insight for innovating new/custom interfaces for a subsystem and their potential impact.

About the Author
Sanjeeb Mishra is a Validation Architect with Intel. He has 15 years of experience ranging from hardware system design to SOC validation for telecom, consumer electronics, PC and mobility products; and has specific expertise on SoC architecture for mobile devices.

Neeraj Kumar Singh is a Platform Architect for tablet platforms at Intel. Prior to this he worked on CPU, Graphics and Chipset validation tools. His areas of expertise are hardware software co-design, SoC system architecture, and system software design and development.

Vijayakrishnan Rousseau is a Technical Lead at Intel. He has 15 years of experience in GPU and SOC validation with specialization in Display interfaces like HDMI, Display Port and Emulation.

## Users Review

**From reader reviews:**

**Bonnie Daves:**

Playing with family in a park, coming to see the sea world or hanging out with good friends is thing that usually you have done when you have spare time, after that why you don't try thing that really opposite from that. One activity that make you not feeling tired but still relaxing, trilling like on roller coaster you are ride on and with addition of information. Even you love System on Chip Interfaces for Low Power Design, you can enjoy both. It is great combination right, you still want to miss it? What kind of hangout type is it? Oh come on its mind hangout men. What? Still don't obtain it, oh come on its referred to as reading friends.

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